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Gabara 73-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T.J. Gabara et al.
Case: 73-1
Serial No.: To Be Assigned
Filing Date: August 13, 1999
Title: Signal Processing Method and Apparatus for
Ensuring a Desired Relationship Between Signals
Group: To Be Assigned
Examiner: To Be Assigned

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying Form PTO-1449: P. Plaza et al., "A 2.5 Gb/s ATM Switch Chip Set," IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 4, No. 3, pp. 405-415, September 1996. A copy of the listed document is enclosed.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,

A handwritten signature in cursive script that reads "Joseph B. Ryan".

Joseph B. Ryan
Reg. No. 37,922
Attorney for Applicants

Date: August 13, 1999
Ryan & Mason, L.L.P.
90 Forest Avenue
Locust Valley, New York 11560
(516) 759-7517